

Dr. Georgios Ch. Sirakoulis received his Diploma degree and Master of Engineering in Electrical and Computer Engineering from the Democritus University of Thrace (DUTH), Greece in 1996. For his Diploma Thesis he received a prize of distinction from the Technical Chamber of Greece (TEE). In 2001, he received his PhD in Electrical and Computer Engineering from the Democritus University of Thrace, Greece. Georgios has served as research associate at the Laboratory of VLSI Design and Testing of the same department for the period 1996-2002. In 2002 he joined the Department of Electrical and Computer Engineering (DECE), Democritus University of Thrace, Greece, as a non-tenured faculty member, where he is currently an Associate Professor with tenure. He has also served as non tenured Lecturer in the Departments of Production Engineering and Management, School of Engineering, and in the Department of Molecular Biology and Genetics, Democritus University of Thrace (2002-2005) and in other Departments of DUTH and Eastern Macedonia and Thrace Institute of Technology. Moreover, he was co-founder and research associate of Ulysses Ltd., Kavala, Greece (1999-2002).

From 2014, Georgios is appointed Head of Division of Electronics and Information Systems Technology of the largest Division of DECE of DUTH. Furthermore, he is serving as Visiting Research Professor in University of West England from 2014 as well as member of Institute of Unconventional Computing. For the period 2014-2015 he has also served as Visiting Professor in Uniwersytet Kardynała Stefana Wyszyńskiego w Warszawie in Warsaw, Poland and for 2013 spring semester in AGH University in Krakow, Poland.

Georgios (Google Scholar - h-index: 23, i-index: 59) has published more than 100 papers in international journals, all included in the Science Citation Index, more than 125 papers in international conference proceedings, 13 guest-editorials in IEEE Transactions on Nanotechnology, Microelectronics Journal, IEEE Computing in Science and Engineering, Journal of Cellular Automata, International Journal of Unconventional Computing, Journal of Computational Science, International Journal of High Performance Computing, Simulation, and ACM TOMACS, respectively. He is co-editor of 5 books (one national and five international all published by Springer) as well as co-author of 17 book chapters.

He serves as Associate Editor in "Microelectronics Journal", "Integration, the VLSI journal", "International Journal of Parallel, Emergent and Distributed Systems", "International Journal of Unconventional Computing," "Journal of Cellular Automata", "Parallel Processing Letters," "Journal of Applied Mathematics", and "Recent Advances on Electrical and Electronic Engineering". He has been reviewer in more than 100 ISI journals as well as in more than 90 International Conferences.

He has participated as leader and principal investigator in more than 19 scientific programs and projects funded from the Greek Government and Industry as well as the European Commission, such as EU AMDREL project IST-2001-34379, which was awarded in VLSI 2005 and ASP-DAC 05 and EU VIEWFINDER project INFSO-IST-045541 as well as coordinator of Memory Management Methodology for Real-Time and Low Power Embedded Multimedia Systems (PENED'99), Design of Low Power Embedded System for Implementation of Multimedia and Wireless LAN Networks (PENED'03-03EΔ593), Supermicro: Energy Management Optimization of Autonomous Hybrid Systems (Greek General Secretariat for the Research and Technology (GSRT) funding for period 2011-2015) and Green Center: Energy-Efficient MPSoCs for Datacenters (GSRT funding for period 2012-2015). He is Substitute Member of the Management Committee of the COST ACTION IC1401 Memristors

- Devices, Models, Circuits, Systems and Applications (MemoCiS) and Member of the Management Committee of the COST ACTION CA15410 Improving Applicability of Nature-Inspired Optimisation by Joining Theory and Practice (ImAppNIO). Moreover, he has served as member of the Hellenic Agency of the EU IDEAS programme from 2009-2013 and he is EUROPRACTICE representative for DUTH for the last eight (8) years. He has also served as proposal and project reviewer for HORIZON 2020, COST 2013-2014, 2014-2015 and 2015-2016, Lloyd's Register Foundation International Consortium of Nanotechnologies, Polish National Science Center, Israeli Ministry of Science and Technology and Greek General Secretariat for the Research and Technology.

Georgios will be the general co-chair of PACET 2017 in Xanthi, Greece and he was general co-chair of ACRI (Cellular Automata for Research and Industry) 2014 Conference, in Krakow, Poland, of the 10th edition of ACRI 2012 Conference in September 2012 in Santorini, Greece and of Panhellenic Electrical and Computer Engineering Students Conference (SFHHMY 5) in 2012, in Xanthi, Greece. For this year, he serves among others as Publication Chair of MEMRISYS 2017 and Special Sessions Chair for PATMOS 2017 and MOCAS 2017. He was co-chair of several Special Sessions in IEEE ICECS 2013, PDP 2014, ICCSA 2014, PCI 2014, CSW HiPEAC 2014, PDP 2015, IEEE NANO 2015, PCI 2015, PDP 2016, IEEE SSCI 2016, PDP 2017, etc. He has been PC member and in some cases Session Chair of 142 Conferences in the past 8 years. He has delivered 18 invited talks all over the world, with the most recent ones in IEEE ICECS 2015 (tutorial), Wise Design 2015, IC4N 2016 and SICC 2017.

He has been and he is supervisor of 7 Ph.D. Theses, 20 Master of Sciences (M.Sc.) Theses and 62 Master of Engineering (M.Eng. - Diploma) Theses. Three (3) of his students have been awarded with the Best Ph.D. Thesis, Master of Science Thesis and Master of Engineering (Diploma) Thesis Awards, respectively, at the Department of Electrical and Computer Engineering, Democritus University of Thrace in the period 2012-2015. One of his students was recipient of the Best IEEE Greek Chapter Diploma Award in the topics of the Analog and Mixed-signal Circuit Design and Technology in 2015.

He is a member of the Institute of Electrical and Electronic Engineering (IEEE), of the IEEE Computer Society, of the Association for Computing Machinery (ACM), of the Institution of Engineering and Technology (IET), of the International Society for Computational Biology (ISCB), International Society of Bionic Engineering (ISBE) and a member of the TEE. He has served and is serving in TCPs of IEEE, like TPC Cellular Nanoscale Networks and Array Computing, TPC IEEE Parallel and Distributed Systems, TPC IEEE CAS Education, IEEE TCP Real Time Systems, etc. He is Vice Chair of IEEE Task Force on Unconventional Computing and he was also founding member and Vice President of the IEEE Student Branch of Thrace for the period 2000-2001.

His research emphasis is on Complex Electronic Systems, Future and emergent electronic devices, circuits, models and architectures (memristors, quantum cellular automata etc.), Novel and Emergent micro-nano systems and circuits, beyond CMOS computing devices and circuits, Memristors, Green and Unconventional computing, High performance Computing, Novel paradigms of computing, Cyber-Physical and Embedded Systems, Bioinspired computation/ biocomputation and bioengineering, Cellular Automata Theory and Applications, FPGAs, Modelling and Simulation.

His courses lab activities are sponsored by ARM, Freescale, Xilinx, Altera and NVidia. More details in <http://gsirak.ee.duth.gr>