Dr. Georgios Ch. Sirakoulis (http://gsirak.ee.duth.gr) received his Diploma degree and Master of Engineering in Electrical and Computer Engineering from the Democritus University of Thrace (DUTh), Greece in 1996. For his Diploma Thesis he received a prize of distinction from the Technical Chamber of Greece (TEE). In 2001, he received his PhD in Electrical and Computer Engineering from the Democritus University of Thrace, Greece. In 2002 he joined the Department of Electrical and Computer Engineering (DECE), Democritus University of Thrace, Greece, as a non-tenured faculty member, where he is currently a Full Professor with tenure. Moreover, he was co-founder and research associate of Ulysses Ltd., Kavala, Greece (1999-2002).

Georgios was the Head of Division of Electronics and Information Systems Technology of DECE (the largest Division of DECE including 18 Professors and Lecturers) from 2014-2018 and he is Chair of the Internal Evaluation Committee (REF) of DECE of DUTH and member of the Evaluation Committee of DUTH. Furthermore, he is serving as Visiting Researcher/Professor in University of West England from 2014 as well as member of Institute of Unconventional Computing.

Georgios has published more than 120 papers in international peer reviewed journals, all included in the Science Citation Index, more than 150 papers in international peer reviewed conference proceedings, 15 guest-editorials in Microelectronics Journal, IEEE Nanotechnology, IEEE Computing in Science and Engineering, International Journal of Unconventional Computing, Journal of Computational Science, Journal of Cellular Automata, International Journal of High Performance Computing, Simulation, and ACM TOMACS, respectively, and he is co-editor of 7 books (one national and six international all published by Springer) as well as co-author of 25 book chapters.

He serves as Associate Editor in “IEEE Transactions on Nanotechnology”, “IEEE Transactions on Computers”, “IEEE Transactions on Circuits and Systems Part II: Express Briefs”, “Microelectronics Journal”, “Integration, the VLSI journal”, “Plos One”, “Journal of Cellular Automata”, “International Journal of Unconventional Computing”, “International Journal of Parallel, Emergent and Distributed Systems”, “Parallel Processing Letters”, and “Electronics”. He has been reviewer in more than 110 ISI journals as well as in more than 130 International Conferences.

He has participated as leader and principal investigator in more than 26 scientific programs and projects funded from the Greek Government and Industry as well as the European Commission, such as EU AMDREL project IST-2001-34379, which was awarded in VLSI 2005 and ASP-DAC 05 and EU VIEWFINDER project INFSO-IST-045541 as well as coordinator of Memory Management Methodology for Real-Time and Low Power Embedded Multimedia Systems (PENED’99), Design of Low Power Embedded System for Implementation of Multimedia and Wireless LAN Networks (PENED’03-03ΕΔ593), Supermicro: Energy Management Optimization of Autonomous Hybrid Systems (Greek General Secretariat for the Research and Technology (GSRT) funding for period 2011-2015) and Green Center: Energy-Efficient MPSoCs for Datacenters (GSRT funding for period 2012-2015). He is the principal investigator of “Memristive nanomaterials and electronic devices for quantum and neuromorphic computing” project funded by GSRT of Greece in the context of Greek-Russian bilateral joint research projects (funding period 2018-2020), of “Crossbar Architectures with 1D1M switching devices for Neuromorphic Computations” funded by EPANED of Greece (funding period 2019-2020) as well as of the “Wave-Based Memristive Devices and Circuits for Classical and non von Neumann Information Processing” funded by H.F.R.I. (funding period 2020-2022). Furthermore, he was Substitute Member of the Management Committee of the COST ACTION IC1401 Memristors - Devices, Models, Circuits, Systems and Applications (MemoCiS) and Member of the Management Committee of the COST ACTIONs CA15410 “Improving Applicability of Nature-Inspired Optimisation by Joining Theory and Practice (ImAppNIO)” and CA16218 “Nanoscale Coherent Hybrid Devices for Superconducting Quantum Technologies (NANOCOHYBRI)”. Moreover, he has served as member of

the Hellenic Agency of the EU IDEAS programme from 2009-2013 and he is EUROPRACTICE representative for DUTh for the last eight (8) years. He was and is also serving as proposal and project reviewer for HORIZON 2020, COST 2013-2014, 2014-2015 and 2015-2016, Lloyd's Register Foundation International Consortium of Nanotechnologies, Polish National Science Center, Israeli Ministry of Science and Technology, International Consortium of Nanotechnologies (ICoN), UK, Science Fund of the Republic of Serbia and Greek General Secretariat for the Research and Technology.

Georgios has been recently the general co-chair of NANOARCH 2019, Qingdao, China and he was general co-chair of NANOARCH 2018 in Athens, Greece, PACET 2017 in Xanthi, Greece, ACRI (Cellular Automata for Research and Industry) 2014 Conference, in Krakow, Poland, of the 10th edition of ACRI 2012 Conference, in Santorini, Greece and of Panhellenic Electrical and Computer Engineering Students Conference (SFHHMY 10 & 5) in 2017, and 2012, respectively, in Xanthi, Greece. For this year, he serves among others as Special Sessions Chair of the 24th IEEE European Conference on Circuit Theory and Design (ECCTD 2020), Special Sessions Chair of the ACRI 2020, and previously as Publication Chair of MEMRISYS 2019, as in 2018 and 2017, Award Committee Co-Chair of IEEE NMDC 2018, Special Sessions Chair for PATMOS 2017 and MOCAST 2017, etc. He was co-chair of several Special Sessions in IEEE ICECS 2013, PDP 2014, ICCSA 2014, PCI 2014, CSW HiPEAC 2014, PDP 2015, IEEE NANO 2015, PCI 2015, PDP 2016, IEEE SSCI 2016, PDP 2017, PDP 2018, ISCAS 2019, ICECS 2019, ISCAS 2020, etc. He has been PC member and is some cases Session Chair of 142 Conferences in the past 8 years. He has delivered 30 invited talks all over the world, with the most recent ones in MEMRISYS 2019, NANO 2019, IEEE NMDC 2018, TPNC 2018, CNNA 2018, AFCA 2017, IC4N 2016, IEEE ICECS 2015 (tutorial), etc., as well as in Schools like the 4th Training International Summer School of MemoCiS 2018, 12th SICC International Tutorial Workshop in ECCTD 2017, etc.

He has been and he is supervisor of 11 Ph.D. Theses, 22 Master of Sciences (M.Sc.) Theses and 75 Master of Engineering (M.Eng. - Diploma) Theses. Three (3) of his students have been awarded with the Best Ph.D. Thesis, Master of Science Thesis and Master of Engineering (Diploma) Thesis Awards, respectively, at the Department of Electrical and Computer Engineering, Democritus University of Thrace in the period 2012-2015. One of his students was recipient of the Best IEEE Greek Chapter Diploma Award in the topics of the Analog and Mixed-signal Circuit Design and Technology in 2015.

He is a member of the Institute of Electrical and Electronic Engineering (IEEE), of the IEEE Computer Society, of the Association for Computing Machinery (ACM), International Society of Bionic Engineering (ISBE) and a member of the TEE. He has served and is serving in TCs of IEEE, like TC Cellular Nanoscale Networks and Array Computing (as secretary), TC IEEE Cellular Nanoscale Networks and Array Computing Technical Committee (CNAAC), NTC Technical Committee on Nanoelectronics, TC IEEE Parallel and Distributed Systems, IEEE TC Real Time Systems, and member of HiPEAC network, IFIP WG 1.5 etc. He is Vice Chair of IEEE Task Force on Unconventional Computing, IEEE Greece Section Membership Development Officer and he was also founding member and Vice President of the IEEE Student Branch of Thrace (2000-2001).

His research emphasis is on Complex Electronic Systems, Future and emergent electronic devices, circuits, models and architectures (memristors, quantum cellular automata etc.), Unconventional Nanoelectronics and Computing, Novel and Emergent micro-nano systems and circuits, beyond CMOS computing devices and circuits, High Performance Computing, Novel paradigms of computing, Neuromorphic Computations, Cyber-Physical and Embedded Systems, Bioinspired computation/ biocomputation and bioengineering, Cellular Automata Theory and Applications, Cellular Neural Networks Theory and Applications, FPGAs, Modelling and Simulation, Robotics and Complex systems.

His courses lab activities are sponsored by ARM, Freescale, Xilinx, Altera and NVidia.